

Research engineer **System-on-chip specialization**

HawAI.tech

HawAI.tech is an innovative startup specializing in the design and commercialization of hardware and software for probabilistic artificial intelligence. It consists today of 10 employees, mainly in the region of Grenoble (France), and also in Paris (France) and Liège (Belgium).

HawAI.tech is looking for talented, driven and creative people to shape a common ambition of becoming a major player of explainable AI, specialized in software design and hardware accelerators dedicated to probabilistic models.

Job description

You will join the hardware design team of HawAI.tech, developing circuits dedicated to hardware acceleration of probabilistic AI models. In this team, and in collaboration with the software design team, you will :

- ▶ Participate in the specification and definition of the computation architecture
- ▶ Elaborate memory storages and transfer strategies
- ▶ Define the circuits communication infrastructure

By joining HawAI.tech, you will evolve in an open and dynamic R&D team, listening to its different collaborators, and developing solutions at the forefront of innovation.

Required profile

Engineer and/or PhD in microelectronics and/or in embedded systems, you justify of a successful experience in the design of a system on chip in full or in part. Ideally, you have a knowledge of memory architectures and many-core architectures.

You are an autonomous, curious, and driven person, who is eager to learn, and you appreciate teamwork and decision-making.

Expected skills

- ▶ VHDL, Verilog, or SystemVerilog
- ▶ Transaction-level modeling
- ▶ Bus protocol implementation (AXI/AMBA)

Welcomed skills

- ▶ Make/Shell/Tcl/Python scripting
- ▶ Notions of cache policies
- ▶ Basic communication skills in French and English

Recruitment terms

- Type of contract** • Permanent contract (CDI)
- Job location** • Grenoble, France
- Start date** • As soon as possible
- Contact** • CV and cover letter to be sent to jobs@hawai.tech